



**TERMINAL DISCLAIMER TO OBTAIN A DOUBLE PATENTING
REJECTION OVER A "PRIOR" PATENT**

Docket Number (Optional)
M-9642 US

In re Application of: Agrawal et al.

Application No.: 10/022,464

Filed: December 14, 2001

For: Block Oriented Architecture For A Programmable Interconnect Circuit

The owner*, Lattice Semiconductor Corporation, of 100 percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application which would extend beyond the expiration date of the full statutory term **prior patent** No. 6,653,861 as the term of said prior patent is defined in 35 U.S.C. 154 and 173, and as the term of said **prior patent** is presently shortened by any terminal disclaimer. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the **prior patent** are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer, the owner does not disclaim the terminal part of the term of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 and 173 of the **prior patent**, "as the term of said **prior patent** is presently shortened by any terminal disclaimer," in the event that said **prior patent** later:

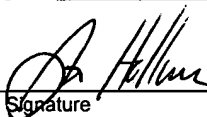
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2. ☒ The undersigned is an attorney or agent of record. Reg. No. 42,622


Signature

February 23, 2006
Date

Jonathan W. Hallman
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(949) 752-7040
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- ☒ Terminal disclaimer fee under 37 CFR 1.20(d) included.

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